ECN#

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ENGINEERING CHANGE NOTE:



ECN#: 3

ASSEMBLY: New Assembly: Current Assy Rev: New Assy Rev:	BMW4091 D (1.1) E (2.0) F (1.2) F (2.1)	DESCRIPTION: DATE: Current Schematic Rev: New Schematic Rev:	MW4- AO1 Analog Output 1 Card 5/05/00
O CMS BOMs Update	ed • Excel BOMs Updated	Subcontractor Records Up	dated Compatibility Maintained: Yes
IMPLEMENTATION Safety Issue Bug Fix Reliability Reason	UL Compliance Issue EMC Compliance Issu	New Feature Quality Issue Cosmetic Change	Cost Reduction
REASON FOR CHANGE: Rectify JTAG Errors.			

DETAILS OF CHANGE:

- 1. Desolder CN1 Pin 32 and bend out about 45 degrees
- 2. Solder a 47R SMD resistor (0805 or 0603) between the lifted pin and its pad.
- 3a. For Rev A PCBs Put a solder blob across J3 and remove the blob across J4 to raise the revision code to 1.2
- 3b. For Rev B PCBs Put a solder blob across J4 to raise the revision code to 2.1
- 4. Place a assembly revision label on the card, marked "F", after the blank PCB P/N so it looks as follows BMW4091A-F (Rev A PCB's) and BMW4091B-F (Rev B PCB's).